



Indian Institute of Science Bangalore

Prof. Mayank Shrivastava
Associate Professor
Department of Electronic Systems Engineering
Indian Institute of Science Bangalore
560012, Bangalore, Karnataka, India

Inquiry Number: DESE/LU/MSA/15/2022-23

Dated: 13/07/2022

Request for Quote from Indian Original Equipment Manufacturer (OEM) or their Indian authorized distributor for the procurement of Quantum measurement and control setup for 2 quantum dot/semiconducting qubit system

**Indian Institute of Science, Bangalore
(Last Date: 3rd August 2022)**

Dear Sir/Madam,

Kindly send your best price quotation (in INR only) for the following item with various accessories on FOR-IISc Bangalore basis to the undersigned. Your quotation should clearly indicate the terms of delivery, delivery schedule, entry tax, payment terms, etc.

Your quote should also include mode of payment and **should reach the undersigned, duly signed on or before 1000 hours (IST) on 3rd August 2022.**

The quote must include all details of technical specifications of the equipment along with the commercial terms and conditions, the bill of materials, printed technical brochure and any other supporting document. **Please enclose a compliance certificate, printed on your letter head, along with the quote.**

The commercial bid must include the price of the item in Indian currency, indicating the following separately:

- FOR price
- Freight and Insurance
- Post-warranty maintenance charges
- Tax
- Total

The quotation should address to:

The Chairman,
Department of Electronic Systems Engineering
Indian Institute of Science, Bangalore – 560012

General Description and specifications:

The purpose of this setup is to control and measure the status of 1 or 2 semiconducting qubits/Quantum dots. This system needs to be integrated with the dilution refrigerator. The system should be able to control and do all the measurements and gating operations on single qubit configuration and two qubits configurations independently.

Below are some of the quantum gates that will be implemented in initial stage of our exploration & instruments/setups that are needed for such measurements are therefore required to be integrated/provided with the quantum measurement setup.

Single Qubit Gates: Pauli X, Y & Z gates, Rotation gates to manipulate, general & special (Hadamard) superposition gates, General & special phase gates (Z, S, T) to manipulate phase angle.

Two Qubit Gates: Controlled NOT gate to implement basic 2 qubit-controlled operations, SWAP Gate to allow swapping

E-mail : mayank@iisc.ac.in (write to msdlab.ese@iisc.ac.in for tender related queries)
Phone : +91-80-2293-2732
Faculty Web : <https://faculty.dese.iisc.ac.in/mayank/>
Institute Web : <http://www.iisc.ac.in/>



Indian Institute of Science Bangalore

Prof. Mayank Shrivastava
Associate Professor
Department of Electronic Systems Engineering
Indian Institute of Science Bangalore
560012, Bangalore, Karnataka, India

of two qubit states, Controlled Phase Gates that enables to manipulate phase of target qubit via a control qubit. All quantum gates should have the ease of integration into quantum circuits at a later stage of research.

Also, if possible we would like to know if an integrated setup can be provided to create Bell states for entanglement operations.

Software:

All the aforementioned operations on single and two qubit systems should be individually performed by the software. Furthermore, we would like to know if a reconfigurable software-controlled setup is possible for performing all single & two qubit gates. To avoid having to build scripts for each action that needs to be performed on semiconducting qubits or quantum dots, this software must already have all the necessary instructions(Inbuilt). The hardware and software should be properly calibrated.

I. Technical Specifications of Different Parts of Qubit measurement setup

The setup may include following parts whose specifications are mentioned below:

1. Ultra-high frequency Lock-in Amplifier

- 600 MHz dual lock-in amplifier, dual signal generator, 4 dual-phase demodulators per channel with LAN and USB ports
- Capability of directly measuring signals in the frequency range from DC to 600 MHz without switching between ADCs.
- Lowest signal input noise of $4 \text{ nV}/\sqrt{\text{Hz}}$ for frequencies higher than 5 MHz.
- Analog signal output with amplitude of more than 1 V and more than 500 MHz (1.5 V up to 600 MHz).
- Lock-in amplifier for frequencies $> 200 \text{ MHz}$ with an input range as small as 10 mV.
- Frequency response analyzer up to 600 MHz.

2. Ultra-high frequency -Multi frequency lock-in amplifier

- Lock-in amplifier with 8 arbitrary frequencies
- Low noise input
- Sampling rate in MSa/s
- DC characteristics, low frequency conductance, and gate transimpedance measurement capability at the same time.

3. Arbitrary Waveform Generator

- Two signal outputs, dual generator engine
- High sampling rate $> 1 \text{ GSa/s}$
- 8 Channels per unit
- Less trigger to output delay
- Less delay for dynamic sequencing with multi-bit input signals

4. Ultra-high frequency digitizer

- Two signal inputs
- High sampling rate $> 1 \text{ GSa/s}$
- Minimum 12 bit resolution
- Memory per channels in hundreds of MSa/s

5. High Density Arbitrary waveform generator

E-mail : mayank@iisc.ac.in (write to msdlab.ese@iisc.ac.in for tender related queries)
Phone : +91-80-2293-2732
Faculty Web : <https://faculty.dese.iisc.ac.in/mayank/>
Institute Web : <http://www.iisc.ac.in/>



Indian Institute of Science Bangalore

Prof. Mayank Shrivastava
Associate Professor
Department of Electronic Systems Engineering
Indian Institute of Science Bangalore
560012, Bangalore, Karnataka, India

- 8 outputs, Frequency=750MHz
- Sampling rate 2.4GSa/s,16 bit output
- Less than 50 ns trigger to output delay
- Less than 150 ns delay for dynamic sequencing with multi-bit input signals

6. Multi frequency Lock-in Amplifier

- Frequency range 500kHz
- One demodulator, field upgradable
- Toolset: Scope, sweeper and spectrum analyzer

7. Setup details:

- Should include a written guide (tutorial) as well as a demonstration of how to integrate numerous components of the measurement setup.
- The setup should include all the hardware and software modules that are necessary for the Quantum measurement setup.

8. Other Necessities

- IQ mixers and directional couplers should be provided.
- Required RF and DC cables and connectors that are needed to connect different equipments together and with the dilution refrigerator should be provided
- RF and DC cable connectors
- Please provide the details about the required equipments that need to be purchased separately (if any).

II. Optional Items:

1. Multiple frequency-Multi demodulators (eg; which can extend number of oscillators and demodulators to a total of 4 and also adds 1 external reference PLL)
2. Multiple frequency-digitizer option (Up to 30 MHz FFT span, sampling rate 60 MSa/s,2,5MSa/channel,16bit and input bandwidth up to 7 MHz etc)
3. Please provide separate letter indicating annual maintenance charges (AMC) post warrantee / guarantee period

III. Additional Items (Must be added to compliance certificate as well):

1. Support: Please provide details of support provided within the warranty period
1. Shipping: The quote must be in FOR – IISc Bangalore
2. Installation: Please list a set of acceptance tests for on-site (vendor) inspection and after installation at IISc Bangalore.
3. Other Options: Necessary spare parts should be quoted as an option.
4. Please include any other options currently available that can be added on in the future.
5. Training: Please state if training is required to operate this instrument, and if yes, please highlight the extent of training provided as part of this purchase and for how many days.

Any setup that has slightly different hardware or configuration, but meets the required purpose is also acceptable.

All of the above-mentioned technical specifications are highly desired. However, lower technical specifications may be considered if the above-mentioned specifications are found to be unsuitable in financial terms. The Institute

E-mail : mayank@iisc.ac.in (write to msdlab.ese@iisc.ac.in for tender related queries)
Phone : +91-80-2293-2732
Faculty Web : <https://faculty.dese.iisc.ac.in/mayank/>
Institute Web : <http://www.iisc.ac.in/>



Indian Institute of Science Bangalore

Prof. Mayank Shrivastava
Associate Professor
Department of Electronic Systems Engineering
Indian Institute of Science Bangalore
560012, Bangalore, Karnataka, India

reserves the right to go for lower specifications taking into consideration its technical preferences and financial constraints. Vendors are encouraged to highlight the advantages of their tools over comparable tools from the competitors.

PI Terms and conditions (should be included in compliance certificate):

1. Necessary training to operate the procured setup and required literature support should be provided without additional cost.
2. In principle onsite installation should be free of cost. The amount of time / day committed by the engineer during installation must be clearly stated.
3. Software upgrade, if any, must be free of cost for next 5 years.
4. The vendor must assure that there are no bugs and glitches with the integration. In case of glitches or bugs at the time of installation, vendor must fix the issues in less than three days from the start date.
5. In case of hardware/software issues or support, vendor should be able to provide required solution within three days.
6. All equipment must be well calibrated before and after installation.
7. Additional quote for an annual maintenance contract should be included for the next 5 years.
8. The vendor should have a good track record of delivering such equipment at universities/research institutions (please furnish the details).
9. Please provide list of customers who have procured your equipment in last 5 years.
10. The vendor should be able to repair and maintain the equipment, once it is installed in India. No travel claims must be made by vendor for servicing during the warranty/guarantee period.
11. The lead time for the delivery of the equipment should not be more than 2 months from the date of receipt of our letter of intent. The smallest lead time will be appreciated. Our expectation is shipment immediately after PO and full or part payment post-installation.
12. On all systems the payment terms will be specified in the commercial proposal and is subject to negotiation.
13. The validity period of the quotation should be 90 days at least.
14. Please provide details of the number of trained personnel in India, who can service the machine.
15. Highlight the system/computer requirement to integrate the setup, if any other than specified in the specifications above.
16. See other Terms & Conditions, guidelines, eligibility criteria etc. in enclosed document in the next pages.

Sincerely,

Prof. Mayank Shrivastava
Associate Professor
Department of Electronic Systems Engineering,
Indian Institute of Science,
Bangalore, Karnataka 560012, India
Secretary (Ms. Rekha's) Contact: 9972525771
(On Behalf of Purchase Committee)
Email: msdlab.esse@iisc.ac.in (for tender related queries)

E-mail : mayank@iisc.ac.in (write to msdlab.esse@iisc.ac.in for tender related queries)
Phone : +91-80-2293-2732
Faculty Web : <https://faculty.dese.iisc.ac.in/mayank/>
Institute Web : <http://www.iisc.ac.in/>

Enclosures / Annexures

Section 1 – Eligibility Criteria for Domestic Tender

Prequalification criteria:

1. The Bidder's firm should have existence for a minimum of 3 years. (Enclose Company Registration Certificate)
2. The Bidder should belong to either class 1 or class 2 supplier distinguished by their "local content" as defined by recent edits to GFR. They should mention clearly which class they belong to in the cover letter.
 - a) Class 1 supplier: Goods and services should have local content of equal to or more than 50%.
 - b) Class 2 supplier: Goods and services should have local content of equal to or more than 20 % and less than 50%.
3. Purchase preference as defined by the recent edits to GFR (within the "margin of purchase reference") will be given to Class-1 supplier.
4. MSME can seek exemption to some qualification criteria. IISc follows GFR2017 for such details
5. The bidder should sign and submit the declaration for Acceptance of Terms and Conditions as per -Annexure 4.
6. The Bidder must not be blacklisted/banned/suspended or have a record of any service related dispute with any organization in India or elsewhere. A declaration to this effect has to be given as per Annexure 3.

Section 2 – Terms and Conditions

A) Submission of Tender:

1. All documentations in the tender should be in English.
2. Tender should be submitted in two envelopes (two bid system).
 - a. Technical Bid (Part-A) – Technical bid consisting of all technical details and check list for conformance to technical specifications.

The technical proposal should contain a technical compliance table with 5 columns.

i. The first column must list the technical requirements, in the order that they are given in the technical requirement below.

ii. The second column should provide specifications of the instrument/product against the requirement. Please provide quantitative responses wherever possible.

iii. The third column should describe your compliance with a “Yes” or “No” only. Ensure that the entries in column 2 and column 3 are consistent.

iv. The fourth column should state the reasons/explanations/context for deviations, if any.

v. The fifth column can contain additional remarks from the OEM. You can use this opportunity to highlight technical features, qualify response of previous columns, or provide additional details, compare your solution with that of your competitors or provide details as requested in the technical requirements table below.

b. Commercial Bid (Part-B) – Indicating item-wise price for the items mentioned in the technical bid, as per the format of quotation provided in tender, and other commercial terms and conditions.

3. The technical bid and price bid should each be placed in separate sealed covers, superscripting on both the envelopes the tender no. and the due date. Both these sealed covers are to be placed in a bigger cover which should also be sealed and duly superscripted with the Tender No, Tender Description & Due Date.

4. The SEALED COVER superscripting tender number / due date & should reach **Chairman Office, Department of Electronic Systems Engineering, Indian Institute of Science, Bangalore – 560012, India** on or before due date mentioned in the tender notice. In case due date happens to be holiday the tender will be accepted and opened on the next working day. If the quotation cover is not sealed, it will be rejected.

5. All queries are to be addressed to the person identified in “Section 1 – Bid Schedule” of the tender notice.

6. The items are required for research purposes, and IISc is a DSIR registered institution, hence eligible for GST exemption (i.e. GST @ 5%). While submitting the price quote, this point must be taken care. For getting GST exemption certificate, successful bidders must submit, a formal request together with Invoice copy and Purchase order copy.

7. GST/other taxes, levies etc., are to be indicated separately. The BIDDER should mention GST Registration and PAN in the tender document (Indian Bidders only).

8. If price is not quoted in Commercial Bid as per the format provided in tender document the bid is liable to be rejected.

9. The Institute reserves the right to accept or reject any bid and to annul the bidding process and reject all bids at any time prior to the award of contract, without thereby incurring any liability to the affected bidder or bidders or any obligation to inform the affected bidder or bidders.

10. Incomplete bids will be summarily rejected.

B) Cancellation of Tender:

Notwithstanding anything specified in this tender document, IISc Bangalore, in its sole discretion, unconditionally and without having to assign any reason, reserves the rights:

- a. To accept OR reject lowest tender or any other tender or all the tenders.
- b. To accept any tender in full or in part.
- c. To reject the tender, offer not conforming to the tender terms.

C) Validity of the Offer:

The offer shall be valid 90 Days from the date of opening of the commercial bid.

D) Evaluation of Offer:

1. The technical bid (Part A) will be opened first and evaluated.
2. Bidders meeting the required eligibility criteria as stated in Section 2 of this Document shall only be considered for Commercial Bid (Part B) opening. Further, Agencies not furnishing the documentary evidence as required will not be considered.
3. Pre- qualification of the bidders shall not imply final acceptance of the Commercial Bid. The agency may be rejected at any point during technical evaluation or during commercial evaluation. The decision in regard to acceptance and/or rejection of any offer in part or full shall be the sole discretion of IISc Bangalore, and the decision in this regard shall be binding on the bidders.

4. The award of contract will be subject to acceptance of the terms and conditions stated in this tender.

5. Any offer which deviates from the vital conditions (as illustrated below) of the tender is liable to be rejected:

a. Non-submission of complete offers.

b. Receipt of bids after due date and time and or by email / fax (unless specified otherwise).

c. Receipt of bids in open conditions.

6. In case any BIDDER is silent on any clauses mentioned in these tender documents, IISc Bangalore shall construe that the BIDDER had accepted the clauses as of the tender and no further claim will be entertained.

7. No revision in the terms and conditions quoted in the offer will be entertained after the last date and time fixed for receipt of tenders.

8. Lowest bid will be calculated based on the total price of all items tendered for Basic equipment along with accessories selected for installation, operation, pre-processing and post-processing, optional items, recommended spares, warranty, annual maintenance contract.

E) Pre-requisites:

The bidder will provide the prerequisite installation requirement of the equipment along with the technical bid.

F) Warranty:

The complete system is to be under warranty period of minimum 1-3 years (year wise breakup value should be shown in the commercial bid) including free supply of consumables, spare parts and data analysis software from the date of functional installation. If the instrument is found to be defective, it has to be replaced or rectified at the cost of the bidder within 30 days from the date of receipt of written communications from IISc, Bangalore. If there is any delay in replacement or rectification, the warranty period should be correspondingly extended.

G) Annual Maintenance Contract:

An annual maintenance contract for a period of at least 2 years post-warranty if the warranty is for 1 year, should be provided on completion of warranty period. The AMC costs will not be considered towards classifying the domestic nature (class 1 or class 2)

of the vendor (see eligibility criteria in section 2). AMC for 1 year is sought for warranty of 2 years, and AMC will be optional for 3 year warranty.

H) Purchase Order:

1. The order will be placed on the bidder whose bid is accepted by IISc based on the terms & conditions mentioned in the tender document.
2. The quantity of the items in tender is only indicative. IISc, Bangalore reserves the right to increase /decrease the quantity of the items depending on the requirement.
3. If the quality of the product and service provided is not found satisfactory, IISc, Bangalore reserves the right to cancel or amend the contract.

I) Delivery, Installation and Training:

The bidder shall provide the lead time to delivery, installation and made functional at IISc, Bangalore from the date of receipt of purchase order. The system should be delivered, installed and made functional within 90 days from the date of receipt of purchase order. The supply of the items will be considered as effected only on satisfactory installation and inspection of the system and inspection of all the items and features/capabilities tested by the IISc, Bangalore. After successful installation and inspection, the date of taking over of entire system by the IISc, Bangalore shall be taken as the start of the warranty period. No partial shipment is allowed. The bidder should also arrange for technical training to the local facility technologists and users.

J) Payment Terms:

100% payments (except AMC) will be released after completion delivery and satisfactory installation subject to TDS as per rules. AMC cost (if ordered), after completion of warranty period) will be released on half-yearly basis at the end of each six months subject to satisfactory services. The AMC will be comprehensive. Price basis must be on FOR-IISc Bangalore basis only. As per GFR no advance payment can be made to domestic vendors, unless an equal amount of bank guarantee is provided.

K) Statutory Variation:

Any statutory increase in the taxes and duties subsequent to bidder's offer, if it takes place within the original contractual delivery date, will be borne by IISc, Bangalore subject to the claim being supported by documentary evidence. However, if any decrease takes place the advantage will have to be passed onto IISc, Bangalore.

L) Disputes and Jurisdiction:

Any legal disputes arising out of any breach of contract pertaining to this tender shall be settled in the court of competent jurisdiction located within the city of Bangalore, India.

M) General:

1. All amendments, time extension, clarifications etc., within the period of submission of the tender will be communicated electronically. No extension in the bid due date/time shall be considered on account of delay in receipt of any document(s) by mail.
2. The bidder may furnish any additional information, which is necessary to establish capabilities to successfully complete the envisaged work. It is, however, advised not to furnish superfluous information.
3. The bidder may visit the installation site before submission of tender, with prior intimation.
4. Any information furnished by the bidder found to be incorrect, either immediately or at a later date, would render the bidder liable to be debarred from tendering/taking up of work in IISc, Bangalore.

Section 3 - Technical Bid

The technical bid should furnish all requirements of the tender along with all annexures in this section and submitted to

The Chairman,

Attn: Prof. Mayank Shrivastava

Department of Electronic Systems Engineering,

Indian Institute of Science

Bangalore – 560012, India

Annexure-1:

Details of the Bidder

The bidder must provide the following mandatory information & attach supporting documents wherever mentioned:

Sl. No	Items	Details
1	Name of the Bidder	
2	Nature of Bidder (Attach attested copy of Certificate of Incorporation/ Partnership Deed)	
3	Registration No/ Trade License, (attach attested copy)	
4	Registered Office Address	
5	Address for communication	
6	Contact person- Name and Designation	
7	Telephone No	
8	Email ID	
9	Website	
10	PAN No. (attach copy)	
11	GST No. (attach copy)	

Signature of the Bidder

Name

Designation, Seal

Date:

Annexure-2:

Declaration regarding experience

To,

The Chairman,

Department of Electronic Systems Engineering,

Indian Institute of Science,

Bangalore – 560012, India

Ref: Tender No: XXXXXXXXXXX

Dated: XXXXX

Supply and/or installation of <Item Name> at Prof. Mayank Shrivastava's Lab, Department of ESE, IISc Bangalore

Sir,

I've carefully gone through the Terms & Conditions contained in the above-referred tender. I hereby declare that my company/firm has ---- years of experience in supplying and/or installing <Item Name>.

(Signature of the Bidder)

Printed Name

Designation, Seal

Date:

Annexure-3:

Declaration regarding track record

To,

The Chairman,

Department of Electronic Systems Engineering

Indian Institute of Science,

Bangalore – 560012, India

Ref: Tender No: XXXXXXXX

Dated: XXXXX

Supply and/or installation of <Item Name> at Prof. Mayank Shrivastava's Lab, Department of ESE, IISc Bangalore

Sir,

I've carefully gone through the Terms & Conditions contained in the above referred tender. I hereby declare that my company/ firm is not currently debarred /blacklisted by any Government / Semi Government organizations/institutions in India or abroad. I further certify that I'm competent officer in my company / firm to make this declaration.

Or

I declare the following

Sl.No	Country in which the company is Debarred /blacklisted / case is Pending	Blacklisted / debarred by Government / Semi Government/Organizations /Institutions	Reason	Since when and for how long
-------	-------------------------------------------------------------------------	------------------------------------------------------------------------------------	--------	-----------------------------

(NOTE: In case the company/firm was blacklisted previously, please provide the details regarding period for which the company/firm was blacklisted and the reason/s for the same).

Yours faithfully

(Signature of the Bidder)

Name

Designation, Seal

Date:

Annexure – 4:

Declaration for acceptance of terms and conditions

To,

The Chairman,

Department of Electronic Systems Engineering

Indian Institute of Science,

Bangalore – 560012, India

Ref: Tender No: XXXXXX

Dated: XXXX

Supply and/or installation of <Item Name> at Prof. Mayank Shrivastava's Lab, Department of ESE, IISc Bangalore

Sir,

I've carefully gone through the Terms & Conditions as mentioned in the above referred tender document. I declare that all the provisions of this tender document are acceptable to my company. I further certify that I'm an authorized signatory of my company and am, therefore, competent to make this declaration.

Yours faithfully,

(Signature of the Bidder)

Name

Designation, Seal

Date:

Annexure – 5:

Details of items quoted:

- a. Company Name
- b. Product Name
- c. Part / Catalogue number
- d. Product description / main features
- e. Detailed technical specifications
- f. Remarks

Instructions to bidders:

1. Bidder should provide technical specifications of the quoted product/s in detail.
2. Bidder should attach product brochures along with the technical bid.
3. Bidders should clearly indicate compliance or non-compliance of the technical specifications provided in the tender document.

Section 4 – Commercial Bid

The commercial bid should be furnished with all requirements of the tender with supporting documents as mentioned under:

S.No	Description	Cat. Number	Quantity	Unit Price	Sub total
1	Essential items noted in the technical specification				
1.a	...(details of essential items)				
1.b	...				
2	Optional items noted in the technical specification				
2.a	...(details of essential items)				
2.b	...				
3	Accessories for operation and installation				
4	All Consumables, spares and software to be supplied locally				
5	Warranty (3 years)				
6	AMC 2 years beyond warranty				
7	Cost of Insurance and Airfreight				
8	CIP/CIF IISc, Bengaluru				

Any additional items

S.No	Description	Cat. Number	Quantity	Unit Price	Sub total

Addressed to

The Chairperson,

Attn: Prof. Mayank Shrivastava

Department of Electronic Systems Engineering

Indian Institute of Science, Bangalore – 560012, India

Section 5 – Checklist

(This should be enclosed with technical bid- Part A)

The following items must be checked before the Bid is submitted:

1. Sealed Envelope “A”: Technical Bid

1. Section 3- Technical Bid (each page signed by the authorized signatory and sealed) with the below annexures:

- a. Annexure 1: Bidders details
- b. Annexure 2: Declaration regarding experience
- c. Annexure 3: Declaration regarding clean track record
- d. Annexure 4: Declaration for acceptance of terms and conditions
- e. Annexure 5: Details of items quoted

2. Copy of this tender document duly signed by the authorized signatory on every page and sealed.

2. Sealed Envelope “B”: Commercial Bid

Section 4: Commercial Bid

Your quotation must be submitted in two envelopes: Technical Bid (Envelope A) and Commercial Bid (Envelope B) superscribing on both the envelopes with Tender No. and due date and both of these in sealed covers and put in a bigger cover which should also be sealed and duly super scribed with Tender No., Tender description & Due Date