## Request for Quote and Specifications of gallium oxide (Ga<sub>2</sub>O<sub>3</sub>) epi-wafer

- Bids are sought from qualified <u>Indian vendors under domestic tender</u> for gallium oxide (Ga<sub>2</sub>O<sub>3</sub>) epi-wafers on Ga<sub>2</sub>O<sub>3</sub> substrate with the specifications mentioned in the table below.
- Quote should come only from Indian Original Equipment Manufacturer (OEM) or their Indian authorized distributor.
- The Bidder should belong to either Class-1 or Class-2 suppliers distinguished by their "local content" as defined by recent edits to GFR. They should mention clearly which class they belong to in the cover letter. a) Class-1 supplier: Goods and services should have local content of equal to or more than 50%. b) Class-2 supplier: Goods and services should have local content of equal to or more than 20% and less than 50%.
- Bidders offering imported products will fall under the category of non-local suppliers. They cannot claim themselves as Class-1 local suppliers/Class-2 local suppliers by claiming the services such as transportation, insurance, installation, commissioning, training, and other sales service support like AMC/CMC, etc., as local value addition.
- Purchase preference as defined by the recent edits to GFR (within the "margin of purchase preference") will be given to the Class-1 supplier.
- MSMEs can seek an exemption to some qualification criteria. IISc follows GFR2017 for such details.
- The quotations should be on FOR-IISc Bangalore basis in INR only.
- Companies need to submit two bids, a technical bid and a commercial bid, in <u>two separate</u> sealed envelopes. The bids should be submitted no later than 21 days from the date of posting of this tender and by 5 pm on the 21<sup>st</sup> day or next weekday in case the 21<sup>st</sup> day falls on a weekend.
- Deviations from the technical specifications requested are allowed. Such deviations must be highlighted and justified. Their acceptance or rejection will be left to the discretion of the technical committee.
- The wafers sought will be used toward academic research at the Centre for Nano Science and Engineering (CeNSE), Indian Institute of Science (IISc). IISc is India's No. 1 academic institution on higher learning and the Center for Nano Science and Engineering is home to one of the best academic fabs in the world.
- The technical response, corresponding to the wafers being offered, should be in the form of a compliance table with at least 5 columns. Serial number in column 1. Each of the numbered technical items below should be addressed in a separate row of the table in column 2. Compliance to this requirement, in Yes/No, deviation from it and justification should be provided in the neighboring columns 3-5. Post the opening of a hard copy of the technical bid the committee will request for a soft copy of the files for further processing. Companies should **NOT** mail soft copies of the files unless specifically requested for.
- Detailed technical specifications of the wafer being offered should be included.

Bids should be sent to Prof. Digbijoy N Nath, CeNSE, IISc, Bangalore, 560012. Direct all questions concerning this acquisition at <u>digbijoy@iisc.ac.in</u>

1.	Primary description	Epitaxial $\beta$ -Ga <sub>2</sub> O <sub>3</sub> wafer on $\beta$ -Ga <sub>2</sub> O <sub>3</sub> substrate
2.	Number of wafers required	1 (One)
Specs of the $\beta\text{-}Ga_2O_3$ substrate on which the epi-stack is realized		
3.	Size	<b>4 inch</b> (~ 100 mm) in diameter
4.	Phase/polymorph	β
5.	Doping	Must be > 10 <sup>18</sup> cm <sup>-3</sup>
6.	Dopant	n-type, either Sn or Si dopant, Activated
7.	Orientation	(001)
8.	XRD FWHM	< 400 arcsec
9.	Thickness	> 500 μm
10.	Offset angle	±1°
Specs of the epi-layers of $\beta$ -Ga <sub>2</sub> O <sub>3</sub>		
11.	Thickness	Between 6 to 10 μm
12.	Dopant type	n-type, Silicon
13.	Doping concentration	Between 1 to 3x10 <sup>16</sup> cm <sup>-3</sup> , activated
	Common Terms and Conditions	
14.	The wafer must be pre-diced into 1 cm x 1 cm sizes	
15.	Shipping: The cost of shipping up to IISc should be included.	