

Department of Physics Indian Institute of Science Bangalore, India, PIN 560012

Pre-Bid Clarification for the Domestic Tender

The following revisions are implemented in the corresponding Tender. In addition, the deadline for the tender submission is extended to <u>8 December</u>, 2023, Friday by 5.00PM.

For Master Nodes

 a. 4th Generation Intel Xeon Processor with at least 16 cores and a peak performance of 1.22 TFLOPS, preferably as a single-socket system (or)

4th Generation EPYC Processor with at least 16 cores and a peak performance of 1.22 TFLOPS, preferably as a single-socket system

b. Memory: 64GB DDR5 at 4800 MHz or higher

For Compute Nodes

a. 4th Generation **Dual** Intel Xeon Processor with at least 32 cores per socket and a peak performance of 4.91 TFLOPS or higher (or)

4th Generation Dual EPYC Processor with at least 32 cores per socket and a peak performance of 4.91 TFLOPS or higher,

b. Memory: 512 GB DDR5 at 4800 MHz or higher; Each node should support 12 DIMM slots, but all slots **need not** to be populated. However, the Memory should be configured in a balanced way utilizing all the memory channels.

GPU Compute Nodes

For only GPU nodes, Platinum or better power supply can be quoted. However, for CPU nodes, Titanium level power supply must be quoted. For L1 comparison, the GPU pricing will not be included in the base configuration.

Network/Interconnect:

a. **One** 24 port x 10 Gigabit b managed switch with necessary cables.

Eligibility Criteria:

- b. The bidder should be in HPC/IT business for at least **3 years**. Support documents should be submitted.
- b. The bidder should have an annual turnover of **Rs. 1 Crores** or above in the last 3 Financial Years. Audited Balance sheets should be submitted.